

Abstract of the Disclosure

A data processor includes a first FIFO and a second FIFO. The first FIFO stores a plurality of types of cue data in a predetermined order, and the second FIFO stores, in parallel with the cue data stored in the first FIFO, the types of the cue data and information about continuity. A monitoring control circuit reads the plurality of cue data of the same type continuously from the first FIFO in response to the information stored in the second FIFO in parallel with the cue data. The back-end processor supplies the cue data to a memory as a single unit. The data processor can solve a problem of a conventional data processor in that it must incorporate FIFOs and FIFO monitoring circuits of the number equal to the number of the data types, and hence it is unavoidable that its circuit scale and cost increases with the number of data types.